

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,085,119 B2  
APPLICATION NO. : 10/623974  
DATED : August 1, 2006  
INVENTOR(S) : Bilotti et al.

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1, line 54, delete "a magnetic" and replace with -- of a magnetic --.

Column 1, line 67, delete "not physical" and replace with -- not a physical --.

Column 2, line 5, delete "as a solid" and replace with -- as solid. --.

Column 2, line 17, delete "hall" and replace with -- Hall--.

Column 3, line 43, delete "maybe" and replace with -- may be --.

Columns 4-5, lines 67-1, delete "first and second and comparators" and replace with -- first and second comparators 36, 38 --.

Column 5, lines 20-21 "output voltage  $-V_{OUT}$ ." and replace with -- output voltage  $V_{OUT}$ --.

Column 5, line 31, delete "vice-versa" and replace with -- vice versa --.

Column 7, line 2, delete " $B_{OP}$  the output" and replace with --  $B_{OP}$ , the output --.

Column 7, line 28, delete "that column" and replace with -- that the column --.

Column 7, lines 63-64, delete "by each the two" and replace with -- by each of the two --.

Column 8, line 4, delete "the inverter the inverter circuit 70." and replace with -- the inverter circuit 70. --.

Column 8, lines 16-17, delete "buffer circuit 68a, 68b" and replace with -- buffer circuits 68a, 68b --.

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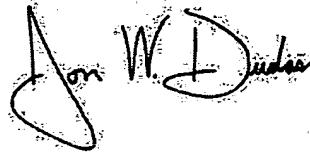
Page 2 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 8, line 62, delete "vice-versa" and replace with -- vice versa --.

Signed and Sealed this

Twentieth Day of March, 2007



JON W. DUDAS  
*Director of the United States Patent and Trademark Office*